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DATE: December 15, 2004**PTO IDENTIFIER:** Application Number 10/707,546-Conf. #1545
Patent Number**Inventor:** Munchiro Karasudani**MESSAGE TO:** US Patent and Trademark Office**FAX NUMBER:** (703) 872-9306**FROM:** CONNOLLY BOVE LODGE & HUTZ LLP

Larry J. Hume

PHONE: (202) 331-7111**Attorney Dkt. #:** 22040-00025-US**PAGES (Including Cover Sheet):** 15**CONTENTS:** IDS (Citation) by Applicant (1 Reference) (1 page)
One (1) non-patent reference (10 pages)
Information Disclosure Statement (2 pages)
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PTO/SB/97 (09-04)

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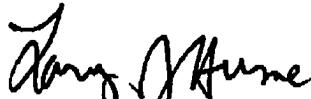
Application No. (If known): 10/707,546

Attorney Docket No.: 22040-00025-US

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Docket No.: 22040-00025-US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Munehiro Karasudani

Confirmation No.: 1545

Application No.: 10/707,546

Filed: December 20, 2003

Art Unit: 2811

For: SEMICONDUCTOR INTEGRATED
CIRCUIT

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 CFR 1.97(b)(3)).

A copy of each reference on the PTO/SB/08 is attached.

A concise explanation of relevance of the items listed on form PTO/SB/08 is that the reference was cited by a foreign patent office during prosecution of a related application.

Application No.: 10/707,546

Docket No.: 22040-00025-US

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 22-0185, under Order No. 22040-00025-US. A duplicate copy of this paper is enclosed.

Dated: December 15, 2004

Respectfully submitted,

By Larry J. Hume
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PTO/3508a/b (08-03)

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Substitute for form 1449A/B/PTO				Complete If Known	
				Application Number	10/707,546-Conf. #1545
				Filing Date	December 20, 2003
				First Named Inventor	Munehiro Karasudani
				Art Unit	2811
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	22040-00025-US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
	CA	"Multiple Twisted Dataline Techniques for Multigigabit DRAM's", Dong-Sun Min and Dietrich W. Langer, IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, pp. 856-865, June 1999.			

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